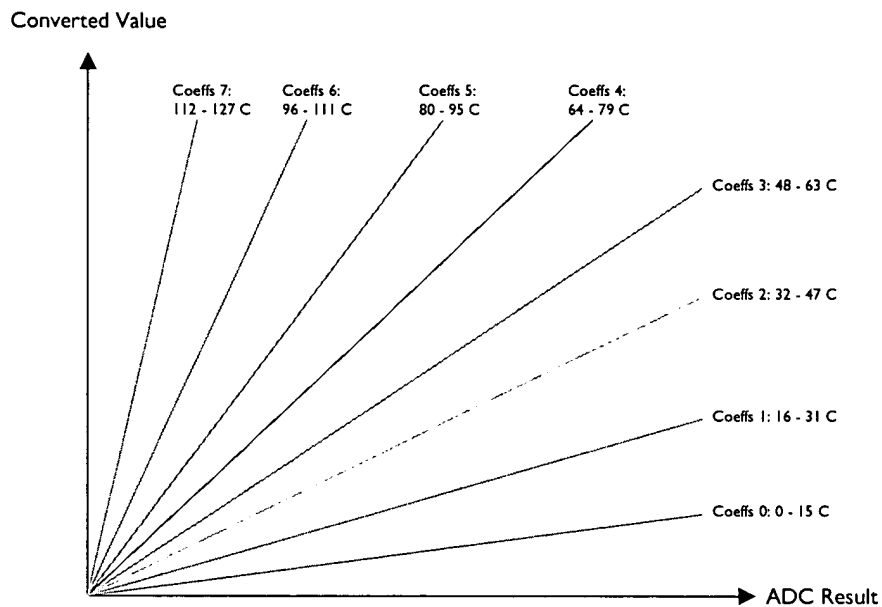


**Figure 1**

	30
0 - 15 C	Slope/Offset 0
16 - 31 C	Slope/Offset 1
32 - 47 C	Slope/Offset 2
48 - 63 C	Slope/Offset 3
64 - 79 C	Slope/Offset 4
80 - 95 C	Slope/Offset 5
96 - 111 C	Slope/Offset 6
112 - 127 C	Slope/Offset 7

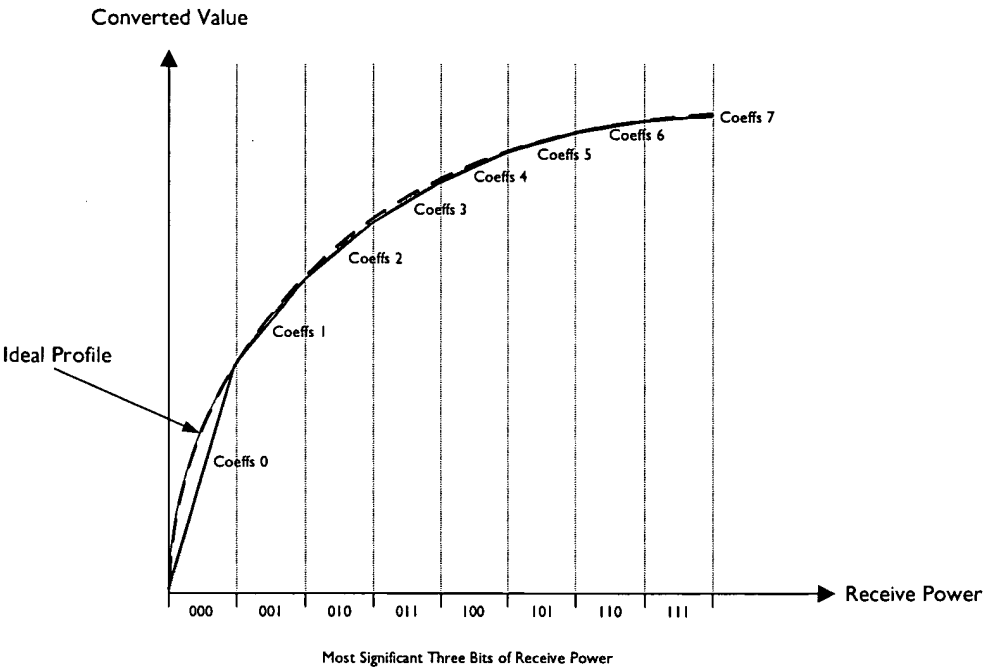
**Figure 2**



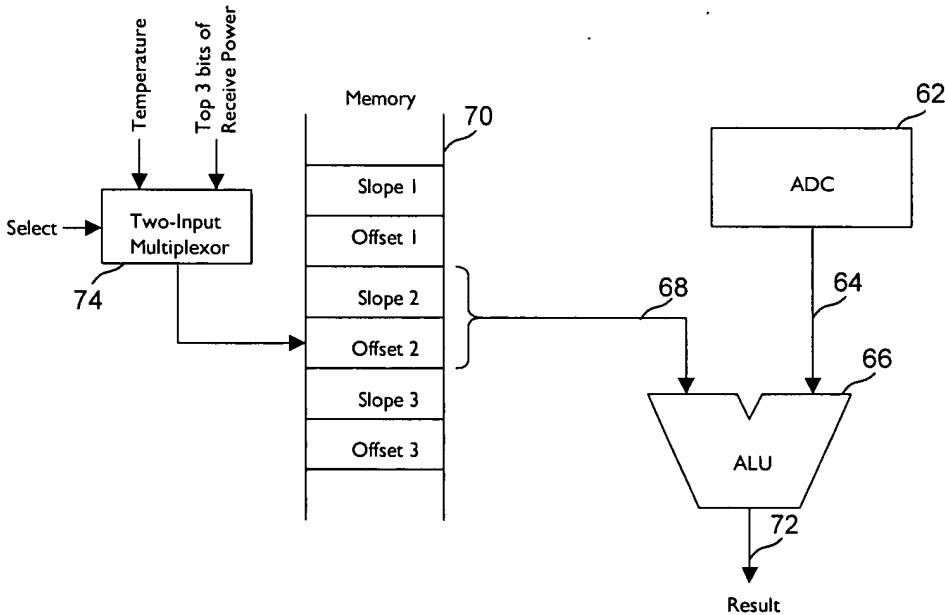
**Figure 3**

Top three bits of the Received Power ADC Result	000	Slope/Offset 0
	001	Slope/Offset 1
	010	Slope/Offset 2
	011	Slope/Offset 3
	100	Slope/Offset 4
	101	Slope/Offset 5
	110	Slope/Offset 6
	111	Slope/Offset 7

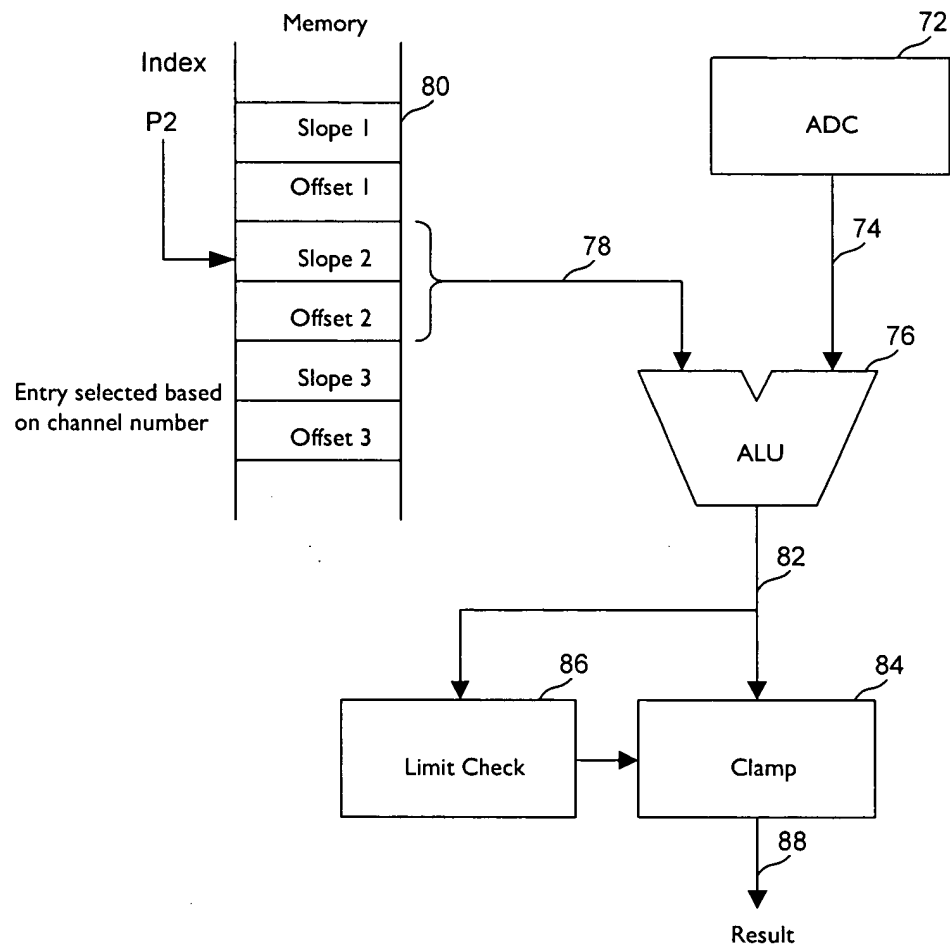
**Figure 4**



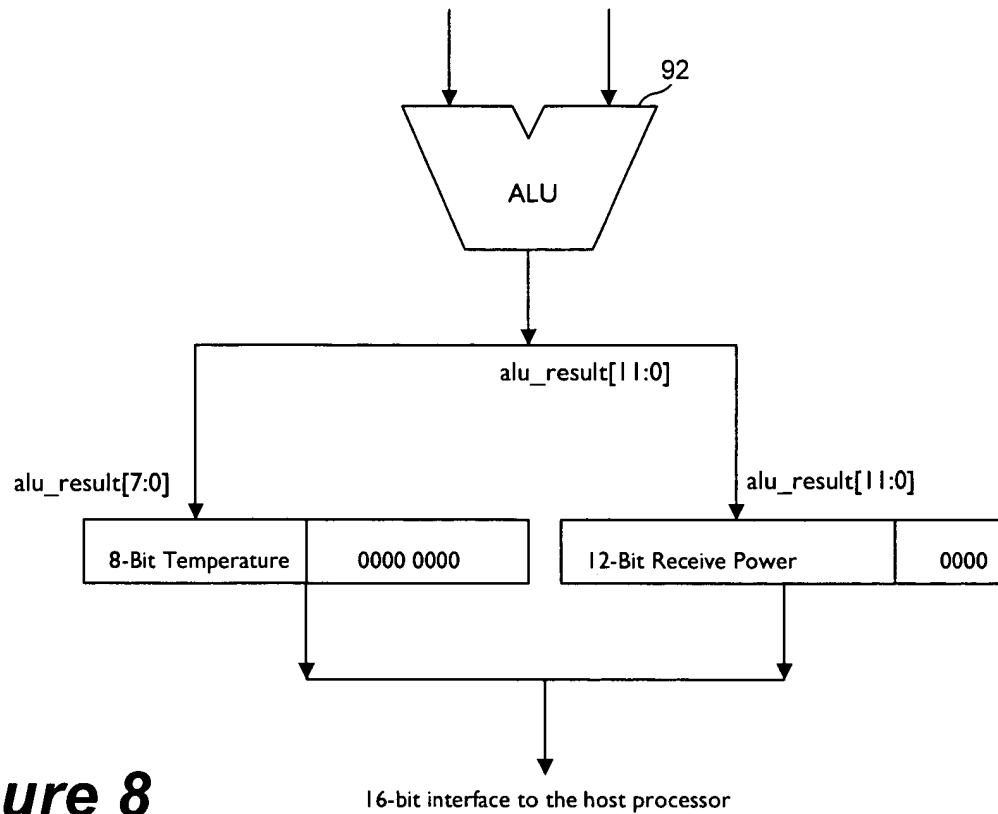
**Figure 5**



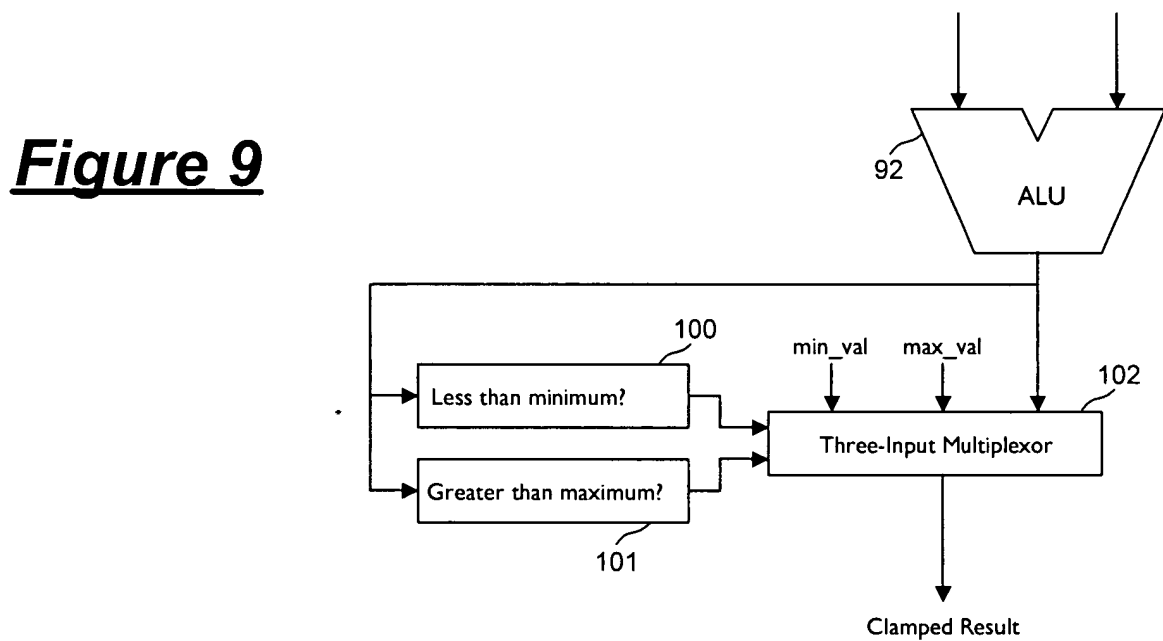
**Figure 6**



**Figure 7**



**Figure 8**



**Figure 9**